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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/718,569	11/21/2000	David Jurasak	8402-6	3104

7590 10/04/2004

CLARK G. SULLIVAN
KING & SPALDING
191 PEACHTREE STREET NE, 45TH FLOOR
ATLANTA, GA 30303-1763

EXAMINER

GEORGE, KEITH M

ART UNIT	PAPER NUMBER
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2663

DATE MAILED: 10/04/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

09/718,569

Applicant(s)

JURASAK ET AL.

Examiner

Keith M. George

Art Unit

2663

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 21 November 2000.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-22 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☒ Claim(s) 14-22 is/are allowed.
- 6) ☒ Claim(s) 1,3 and 5 is/are rejected.
- 7) ☒ Claim(s) 2,4 and 6-13 is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☒ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 21 November 2000 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☒ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date 2.
- 4) ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____.
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: _____.

DETAILED ACTION

Specification

1. The disclosure is objected to because of the following informalities:
 - a. On page 3, line 20, "However, instead using" should probably read "However, instead of using";
 - b. On page 4, line 30, "no inverter is need at the receiver" should probably read "no inverter is needed at the receiver".Appropriate correction is required.

Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

3. Claims 1, 3 and 5 are rejected under 35 U.S.C. 102(b) as being anticipated by Entani, U.S. Patent 5,371,741, hereinafter Entani.
 4. Referring to claim 1, Entani teaches a method and arrangement of multiplexing a plurality of serial data using wired-OR (wired-OR junction producing a time-multiplexed data stream). The serial data are respectively generated from a plurality of serial data generators in a predetermined order (plurality of circuits producing a plurality of output signals) (abstract).
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5. Referring to claim 3, Entani teaches the method described in reference to claim 1 above and teaches in figure 3 that the determiner 20 includes a 2-bit pre-loadable up-counter 32, two inverters 34 and 36 and an AND gate 38 (signals are combined in an AND function to produce a gating signal) (column 1, lines 67 - column 2, line 4).

6. Referring to claim 5, Entani teaches the method described in reference to claim 1 above and teaches in figure 1 a pull-down resistor 16 to tie the wired-OR junction to ground (a pre-charging circuit for actively driving the junction to a ground voltage) (column 1, lines 52-56).

Allowable Subject Matter

7. Claims 14-22 are allowed.

8. Claims 2, 4 and 6-13 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Conclusion

9. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

a. Babb, U.S. Patent 4,044,336, teaches signals combined in a wired-OR connection to produce a multiplexed signal on a byte-wide common output path.

b. Garrett, Jr. et al., U.S. Patent 6,708,248, teaches the "wired OR" nature of the data bus readily facilitates bit-line multiplexing by a number of memory devices.


Any inquiry concerning this communication or earlier communications from the examiner should be directed to Keith M. George whose telephone number is 571-272-3099. The examiner can normally be reached on M-Th 7:00-4:30, alternate F 7:00-3:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Chau T. Nguyen can be reached on 571-272-3126. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



Keith M. George
28 September 2004



CHI PHAM
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2600 9/30/04